

FIG. 1

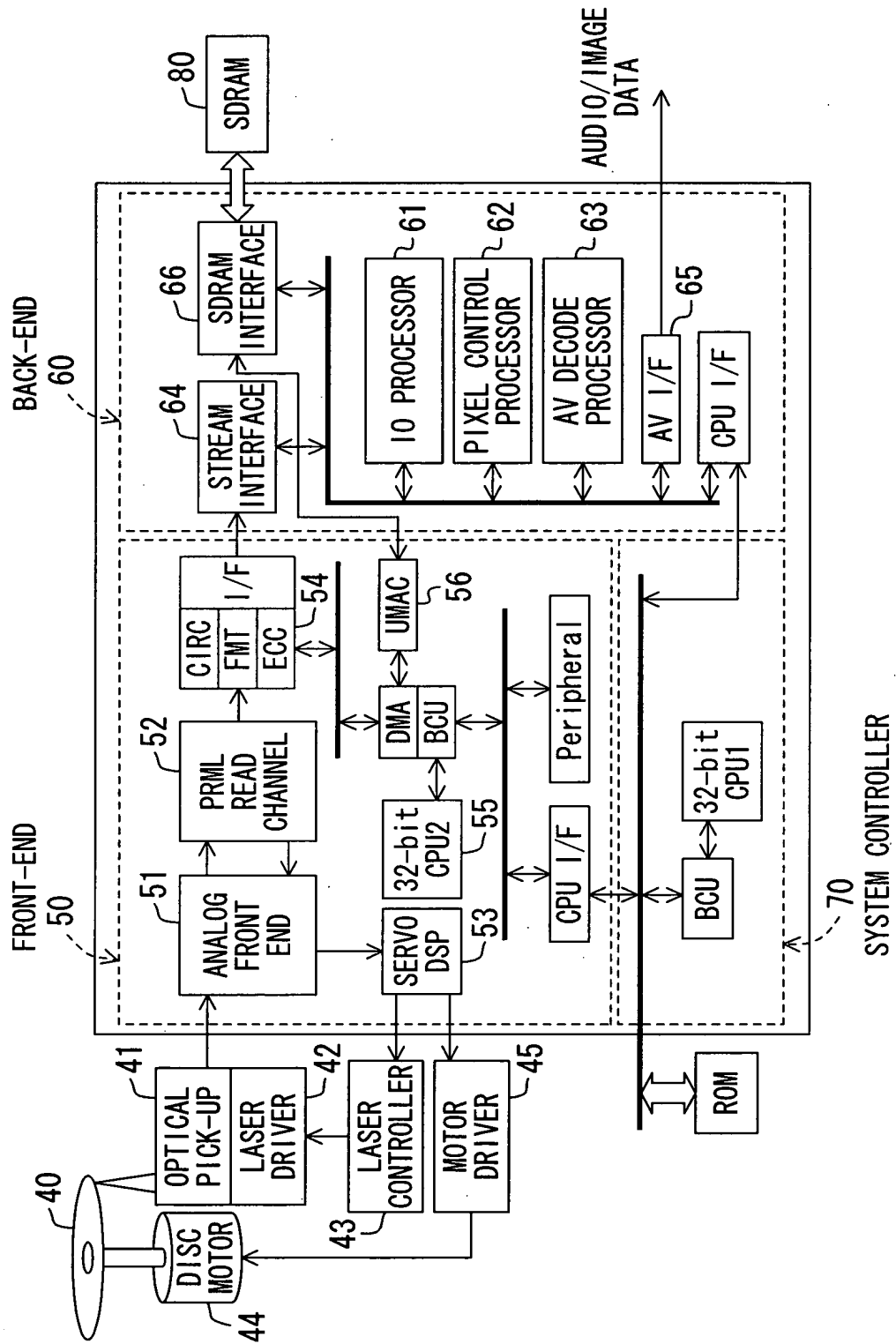


FIG. 2

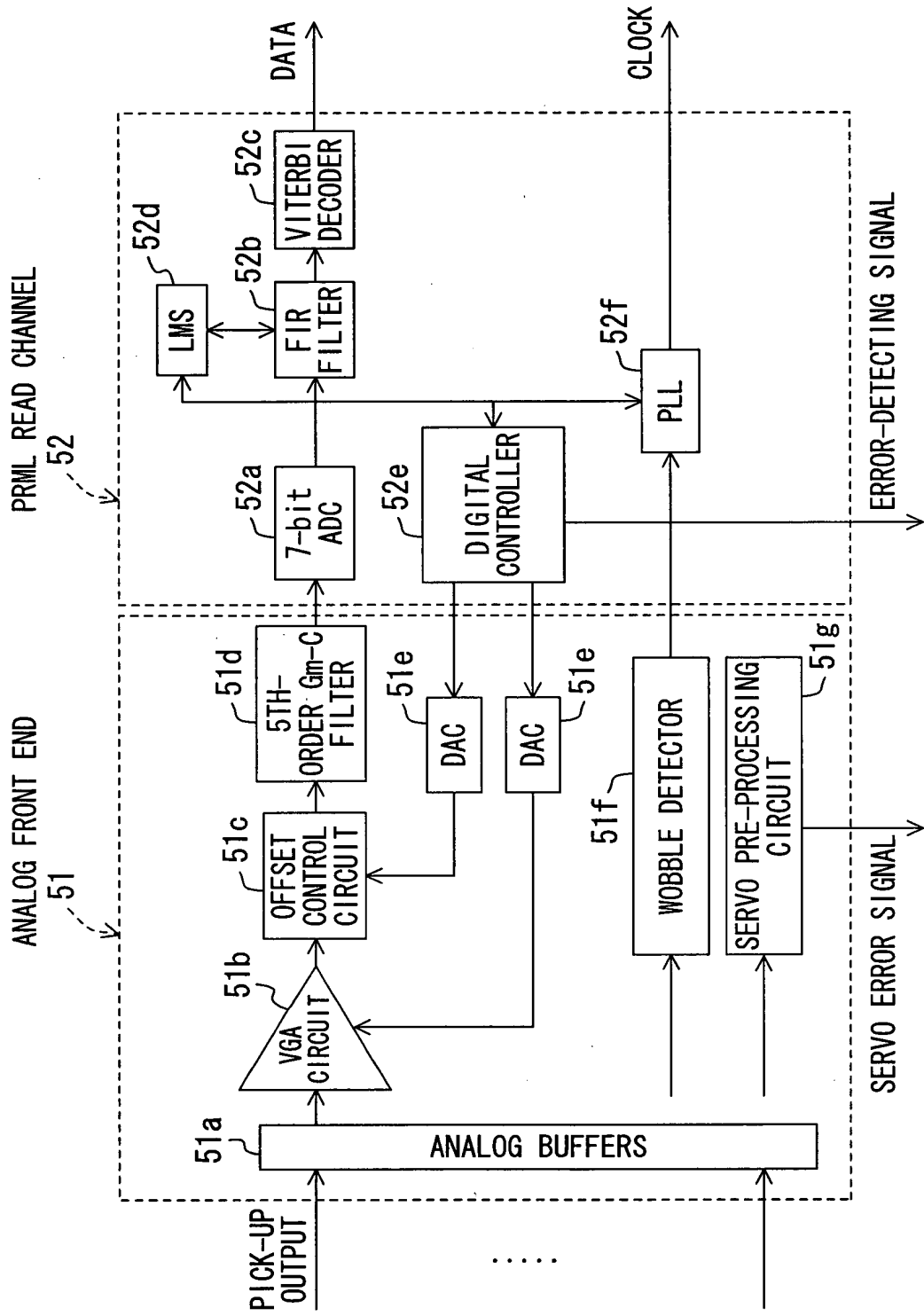


FIG. 3

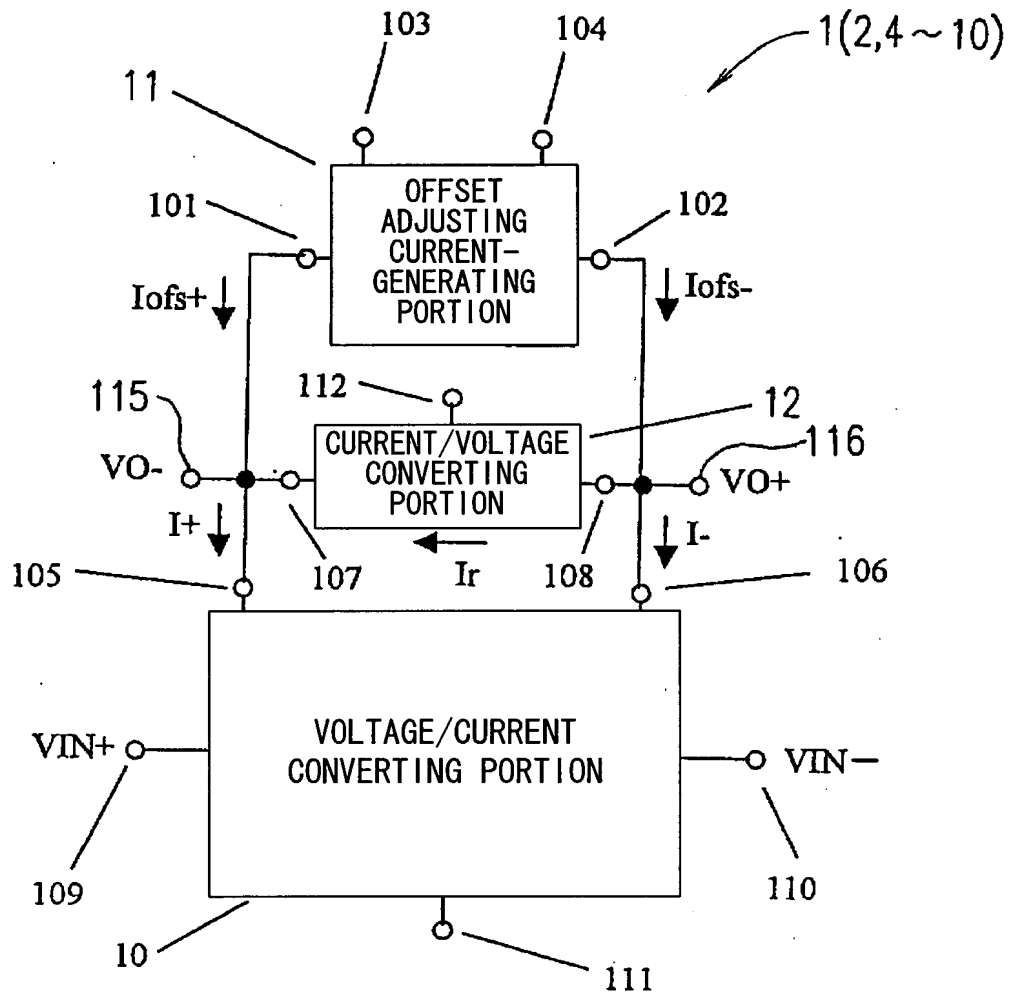




FIG. 5

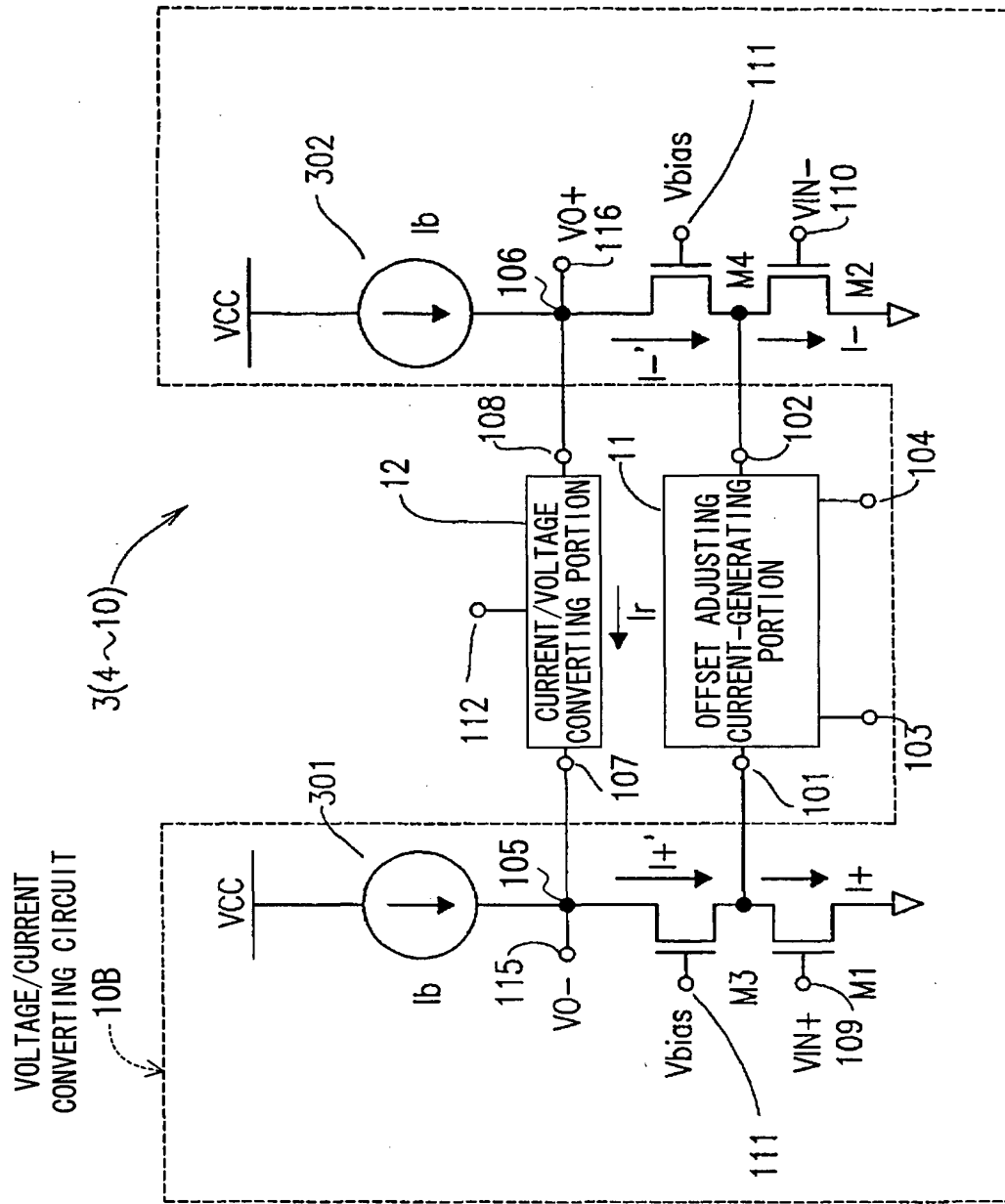


FIG. 6

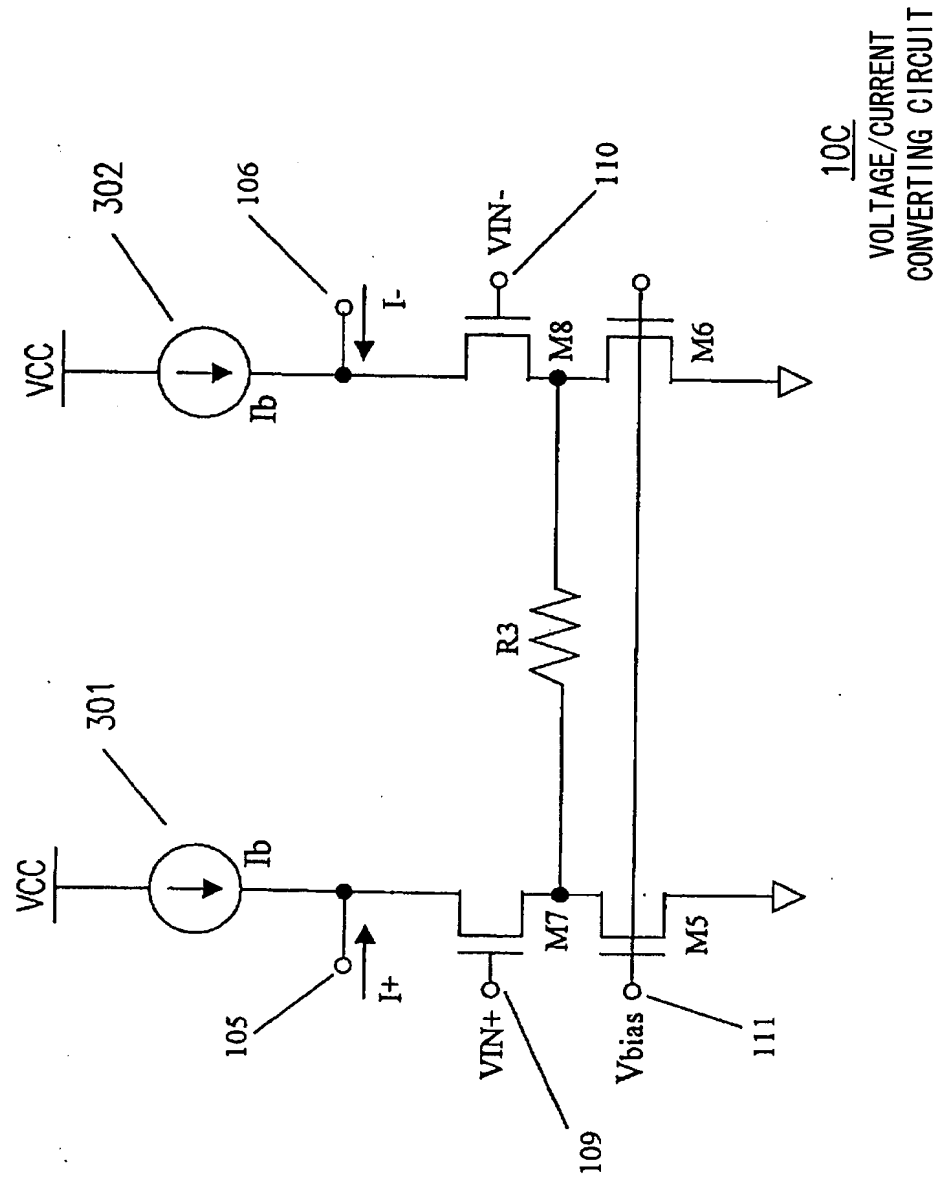
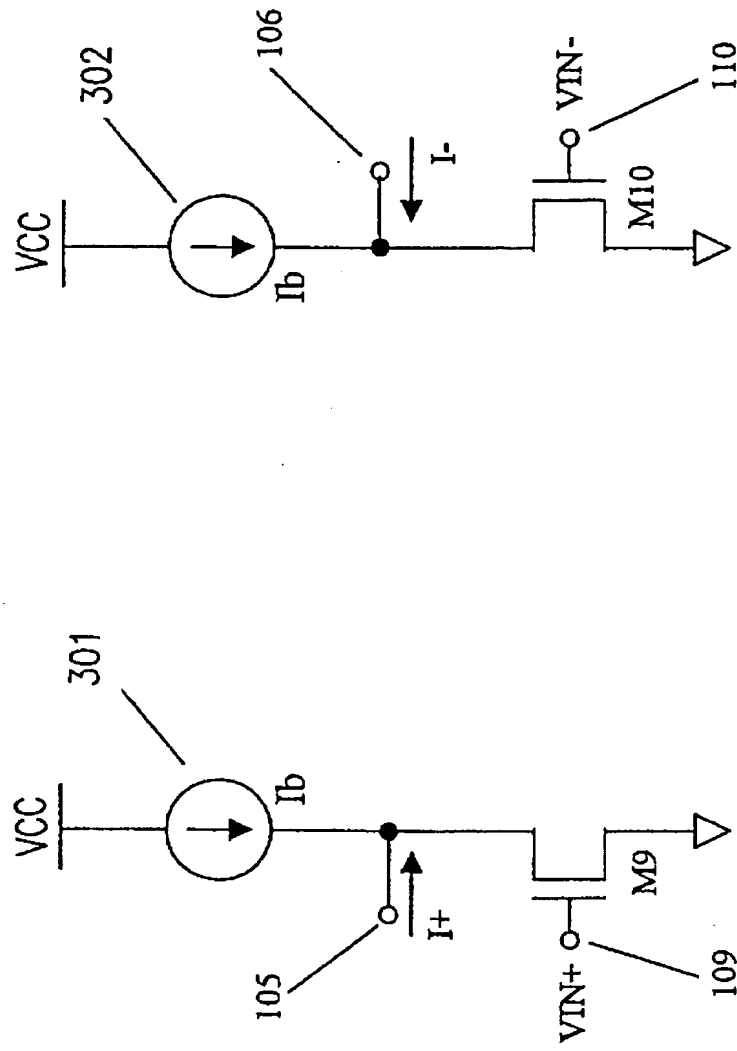


FIG. 7



10D  
VOLTAGE/CURRENT  
CONVERTING CIRCUIT

FIG. 8

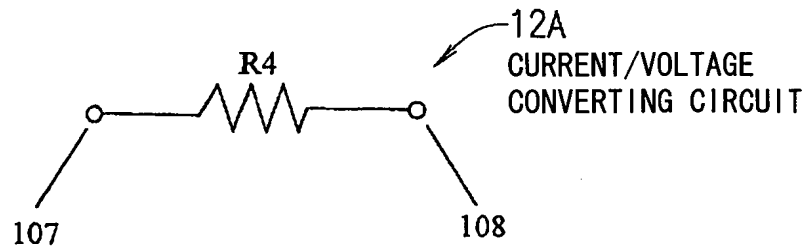


FIG. 9

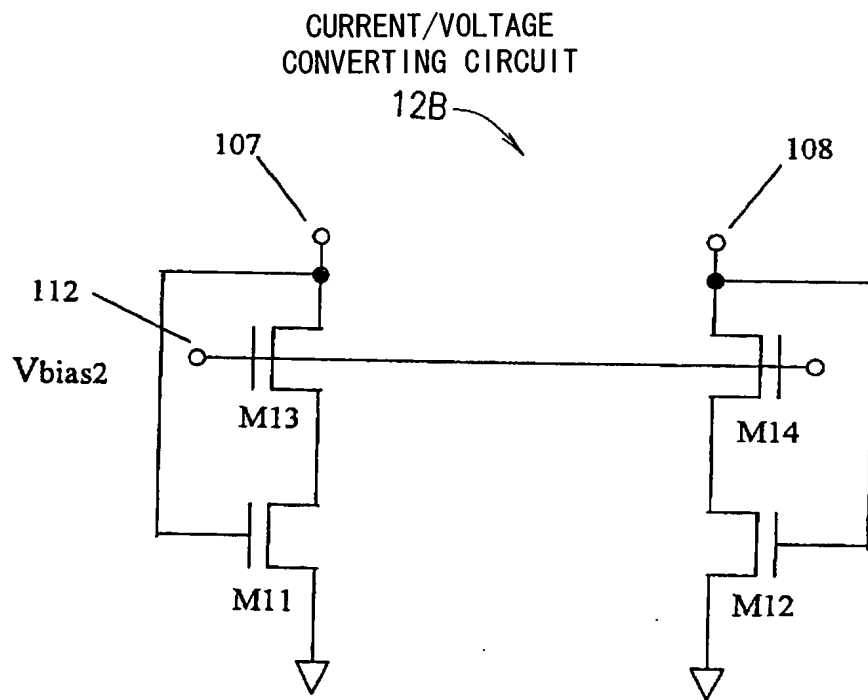




FIG. 10

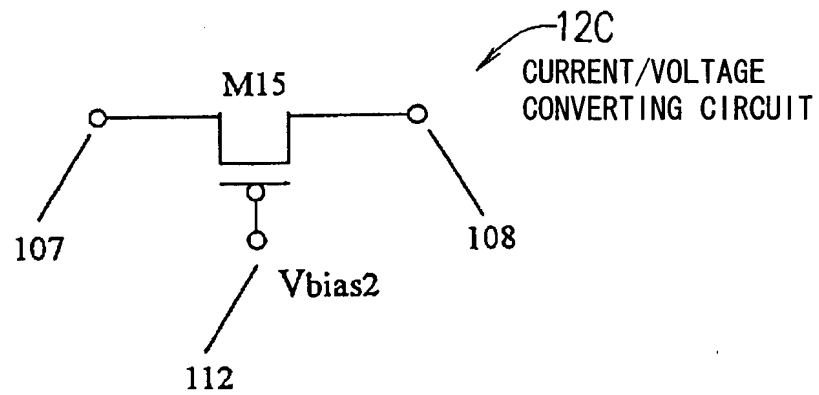


FIG. 11

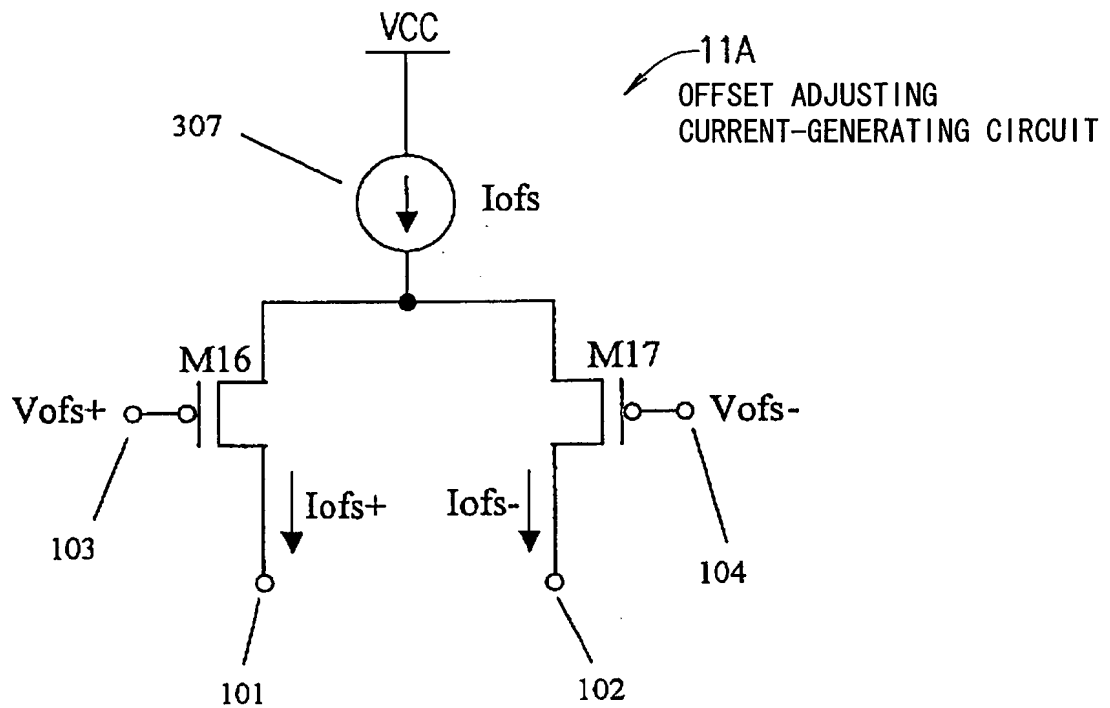


FIG. 12

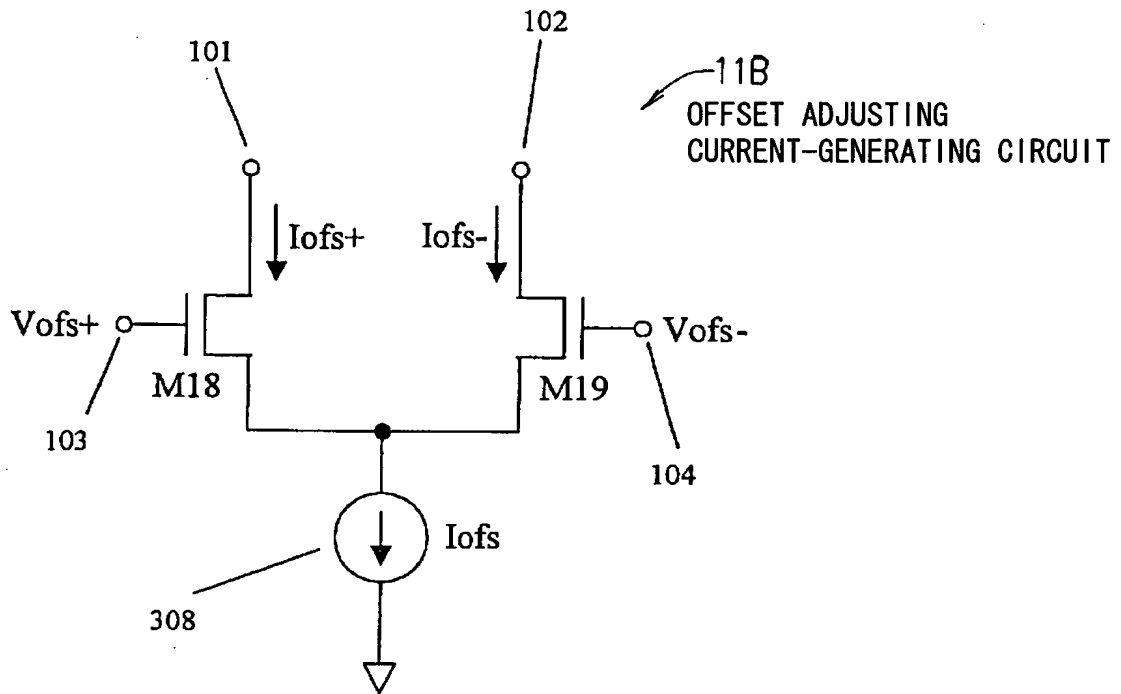


FIG. 13

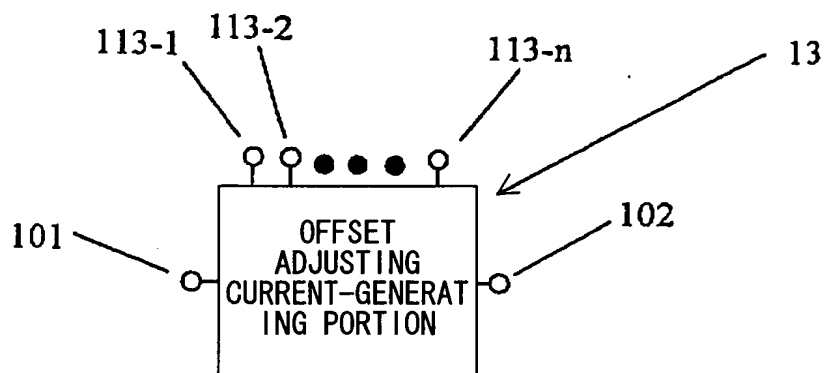
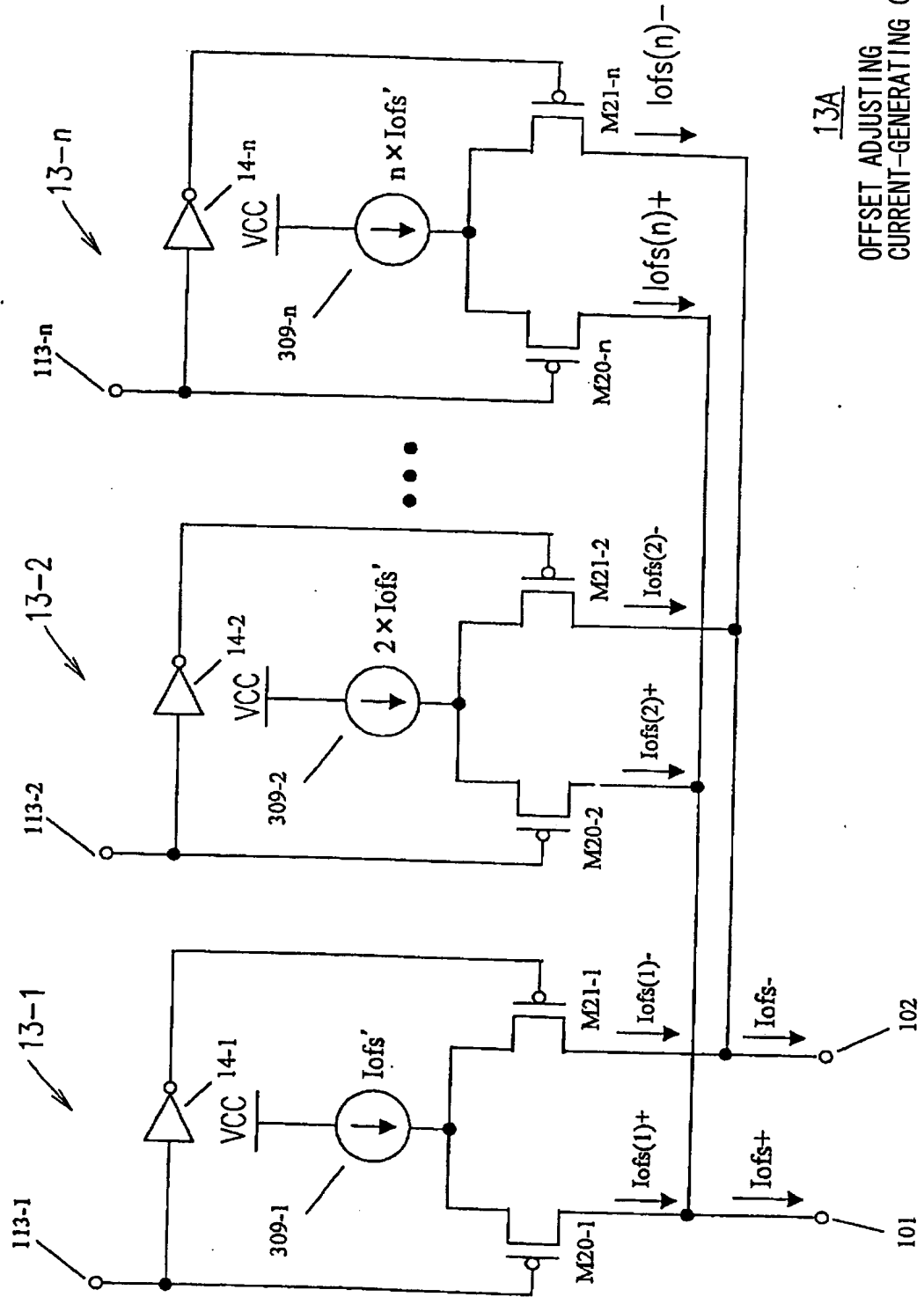


FIG. 14



13A

OFFSET ADJUSTING  
CURRENT-GENERATING CIRCUIT

FIG. 15

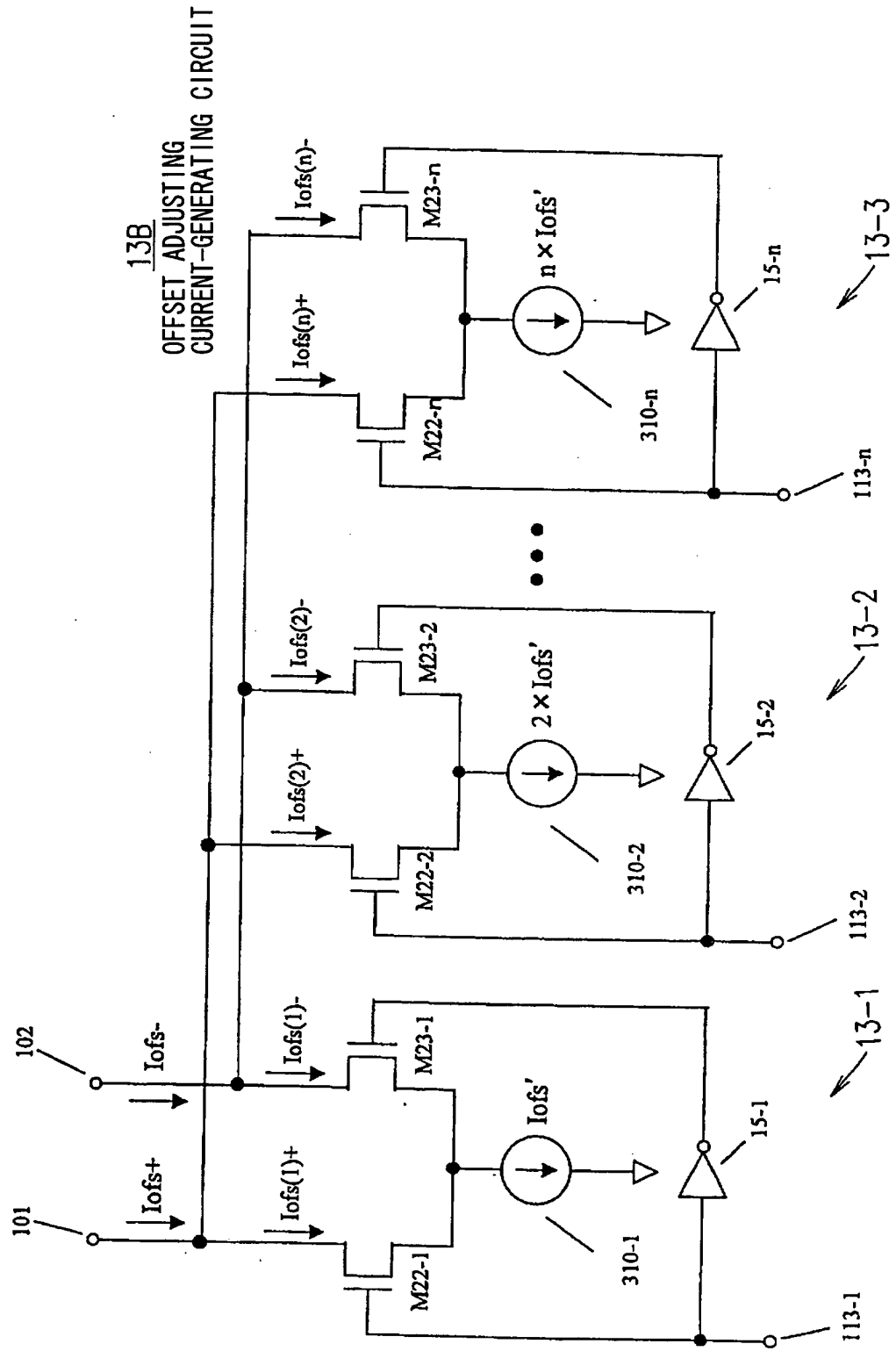


FIG. 16  
PRIOR ART

